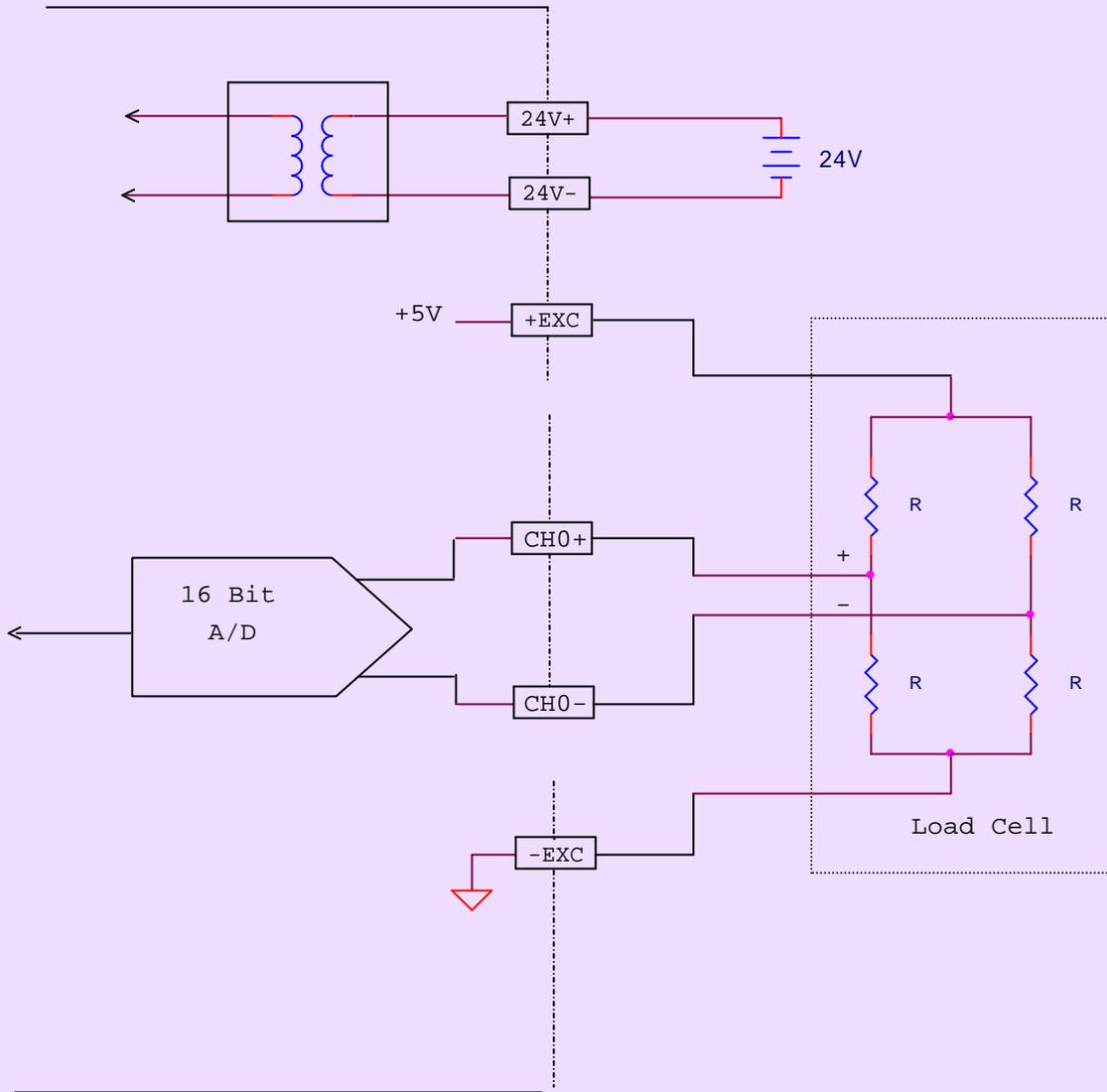


Wiring Diagram



The conversion result is represented by a 16 bit signed value, there should put an additional LCNV (FCN33) or MLC(FCN34) function instruction in the ladder diagram, which will convert the raw reading value into the desire weight value. Because the measurement signal is quite small, for common practice, manual zero adjustment is required in order to overcome the null drift.

PLC Control

The interface between PLC and 1LC module is thru 8 Pts. Of DO and one input register (RI). Thru the control of DO signal, the user can select the conversion rate, operating range and samples for average. Detail description of DO is listed at below. Y_s is the starting number of DO allocated for this module. The conversion result is carried in RI with 16 bit signed format.

Signal	Name	Function Description	
Y_{s+1}, Y_{s+0}	SPAN	00	0~10mV(2mV/V)
		01	0~25mV(5mV/V)
		10	0~50mV(10mV/V)
		11	0~100mV(20mV/V)
Y_{s+2}	Speed Range* ₁	=0, Normal Speed =1, High Speed	
Y_{s+3}	RESERVED	Reserved	
Y_{s+5}, Y_{s+4}	CONVERSION RATE $Y_{s+2} = 0$	00	5Hz
		01	10Hz
		10	25Hz
		11	30Hz
	CONVERSION RATE $Y_{s+2} = 1$	00	60 Hz
		01	-
		10	80 Hz
		11	-
Y_{s+7}, Y_{s+6}	AVERAGE COUNT	00	No Average
		01	2 Samples
		10	4 Samples
		11	8 Samples
RI	RAW WEIGHT	16-bit signed value. The special value -32760 indicates input over range.	

Note*₁: This feature is supported after V1.2 (include) firmware